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Power Frequency Interference and Suppression in Measurement of Power Transmission Tower Grounding Resistance

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Abstract—Due to the electromagnetic induction between transmission lines and the associated grounding wires, power frequency current causes flow into the ground network via the metallic tower, which may interfere with grounding resistance measurements. This paper analyzes the mechanisms of this interference, and shows that power frequency interference becomes stronger at phase-wire transposition towers. Grounding resistance measurement by the variable frequency (VF) method has been proven to be effective in dealing with power frequency interference. However, the test equipment based on the VF method is always deactivated when the external interference voltage exceeds a preset level in order to avoid false readings. This paper aims at tackling the problem by introducing a reverse phase cancellation (RPC) method, which acquires and processes the interference signal before generating a cancelling signal with the same amplitude and frequency and a phase which is exactly opposite in polarity. This paper proves that the RPC method represents an excellent improvement over the VF method since it retains the advantages of VF and avoids the trip-out of test equipment due to excessive interference.

Index Terms—Grounding resistance measurement, power frequency interference, reverse phase cancellation method, transmission tower, variable frequency method.

I. INTRODUCTION

THE GROUNDING resistance of a transmission tower is an important parameter in lightning protection [1], [2], because it affects lightning overvoltages [3], [4]. It is therefore necessary to measure the grounding resistance annually to determine if it is within an acceptable level [5].

When applying the fall-of-potential (FoP) method [6], [7] to measure the grounding resistance, test equipment is often employed, which is connected to the grounding network by a test

wire for injecting power frequency current, and two wires connected to grounding electrodes for collecting return current and measuring the voltage drop. With the ratio of the return current and voltage drop, the grounding resistance can be determined [8]–[11]. However, the FoP method is vulnerable to interference due to the voltage induced in the test object. This is due to mutual inductance between the grounding wires on top of the transmission tower and the high-voltage (HV) transmission lines. The VF method uses the Fourier transformation to analyze the spectrum of the interference signal and then selects a frequency which is not interfered with for testing. However, in order not to provide false readings, the test equipment is always deactivated when the external interference voltage exceeds a preset level, meaning that this method cannot be relied upon when interference is strong.

This paper analyzes the source and mechanism of power frequency interference during measurement of transmission tower grounding resistance, and proposes a reverse phase cancellation (RPC) method as a means of eliminating this interference.

II. ANALYSIS OF THE MECHANISM OF THE POWER FREQUENCY INTERFERENCE

A. Analysis of the Interference Current

From frequency spectrum analysis carried out by the authors, the main interference during grounding resistance measurement was found to be power frequency interference. This agrees with earlier observations [12].

In order to protect transmission lines from lightning, a high-voltage transmission-line system (220 kV or above) in a lightning area is required to have two grounding wires [13], which are physically positioned above the three-phase lines.

Due to the mutual inductance between the overhead transmission lines and the grounding wires, current is induced in each of the grounding wires and flows to ground via the transmission tower and the grounding network [14]–[17]. This power frequency current disturbs the measurement.

Because of the mutual inductance between the grounding wire and the transmission lines, any imbalance in load current among the three phases would cause a current to flow in the closed loops formed by the grounding wires and the ground between towers, as shown in Fig. 1.

The equivalent circuit model is given in Fig. 2. $Z_{n,1}$ and $Z_{n,2}$ are the self-inductances of the two grounding wires, and

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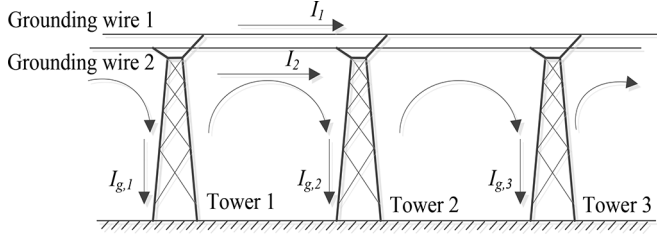


Fig. 1. Principle of interference current flowing in a power transmission system.

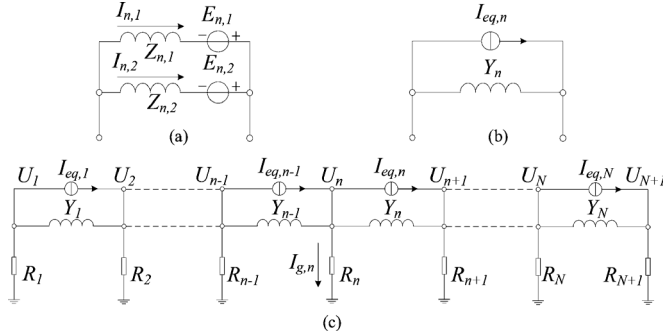


Fig. 2. Equivalent circuit representing the flow of interference current: (a) representation of the grounding wire between two towers, (b) the Norton equivalent circuits of (a), and (c) the nodal model for representing the inductance of the power lines.

$E_{n,1}$ and $E_{n,2}$ are the induced electromotive forces on these grounding wires. The values of $E_{n,1}$ and $E_{n,2}$ depend on the detailed design of a tower. The factors include the physical size of the tower, the distances between conductors, and the way conductors are transposed.

A circuit model of the grounding wires between towers is illustrated in Fig. 2(a). For simplicity, the loop current method is used to solve the equations. Fig. 2(b) shows the Norton equivalent circuits. Formulas (1) and (2) provide the admittances of the equivalent circuit and the equivalent source

$$Y_n = \frac{Z_{n,1} + Z_{n,2}}{Z_{n,1}Z_{n,2}} \quad (1)$$

$$I_{eq,n} = \frac{E_{n,2}Z_{n,1} + E_{n,1}Z_{n,2}}{Z_{n,1}Z_{n,2}}. \quad (2)$$

Based on the simplified model in Fig. 2(b), a simplified overhead ground line model can be deduced as Fig. 2(c). The nodal method can be applied to solve U_n from the equivalent circuit. Formulae (3)–(5), as shown at the bottom of the next page, are the voltage calculation equations of different nodes. Formulae (6)–(10), shown at the bottom of the next page, are the equations for analysis of the interference current in a transmission-line system deduced from (3)–(5). Equation (12) gives the node current flowing into the ground from the tower, as shown at the bottom of the next page.

Depending on the transposition arrangement of the transmission lines, elements in the transposition coefficient matrix $[K]$, as shown in (9), take different values. Their absolute values are equal to one when conductors are not transposed and are always greater than one when conductors are transposed [18]–[20]. From (11) and (12), it is obvious that current flowing from the tower to ground is greater when conductors are transposed.

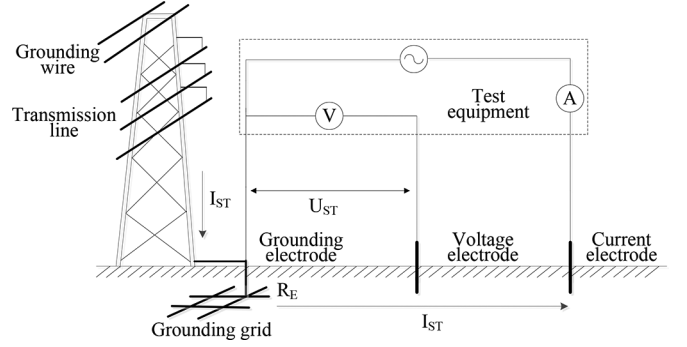


Fig. 3. Schematic diagram of the grounding resistance test using the FoP method.

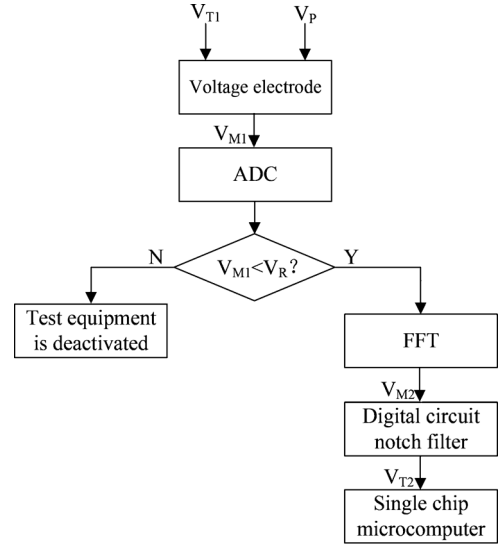


Fig. 4. Block diagram of the VF method.

B. Soil Resistivity

Fig. 3 shows a schematic diagram of how the VF method is applied to measure grounding resistance, where the measurable power frequency interference voltage $U_{ST} = I_{ST} \cdot R_E$. I_{ST} is the current flowing into the ground, and R_E is the grounding resistance. From Ohm's law, the greater value of the grounding resistance, the greater value of U_{ST} . Commonly found in mountainous areas, the grounding resistance of a transmission tower is greater than 10Ω [21].

In summary, a high level of current flowing into the ground and great grounding resistance are the main reasons for strong power frequency interference. This paper proposes a new method of measuring low-frequency tower footing resistance on in-service transmission lines.

III. MECHANISM OF EXCESSIVE POWER FREQUENCY INTERFERING WITH THE VF METHOD

When test equipment employing the VF method is applied, a current square wave of variable frequency is injected into the grounding network [22], [23]. The acquired analog signal is first converted into digital signal through the analog-to-digital converter (ADC) before being converted into the frequency domain by fast Fourier transformation (FFT). A digital notch filter is then applied to remove the power frequency interference. Finally, the value of grounding resistance is calculated. The schematic diagram of the VF method is shown in Fig. 4.

Fig. 4 illustrates the flowchart of the VF method, where V_{T1} (analog signal) is the variable frequency voltage generated by the equipment for measurement of the grounding resistance; V_P (analog signal) is the power frequency interference voltage which is inherent in the power network; V_{M1} (analog signal) is the sum of V_{T1} ; and V_P ; V_R is the ceiling value for the signal voltage, above which the equipment ceases to function; V_{M2} (digital signal) is the voltage signal (V_{M1}) in frequency domain; and V_{T2} (digital signal) is the variable frequency testing voltage in the frequency domain following the removal of power frequency interference.

Disconnection of the equipment occurs when V_{M1} exceeds a threshold value in order to avoid false readings caused by heavy signal attenuation [24]. In the examples presented, the threshold value V_R is fixed at 50 V and the measurement voltage V_{T1} is assumed to be 30 V. Fig. 5(a) shows the case when the interference voltage is 10 V. In this case, V_{M1} is equal to 40 V, which is below the threshold voltage of 50 V, and the results generated by the equipment reflect the real situation. When the interference voltage reaches 55 V, V_{M1} attains a level of 85 V, thus exceeding the threshold value. As shown in Fig. 5(b), the

equipment gives a reading below the true value due to attenuation within the analog circuitry.

Although the VF method is capable of alleviating the interference during the measurement of grounding resistance, it is not applicable when excessive power frequency interference is present. In China, more than 60% of HV transmission towers are located in mountainous areas, meaning that for a significant proportion of these HV transmission towers, their grounding resistance cannot be measured accurately by using either the FoP method or the VF method due to the reasons discussed before.

IV. PROPOSED RPC METHOD FOR REDUCING POWER FREQUENCY INTERFERENCE

A. Principle of RPC Method

Fig. 6 shows the principle of the test equipment employing the proposed interference cancellation device, connected between the voltage electrode and the test equipment. This device generates a cancellation voltage signal which has the same amplitude and frequency as the interference voltage signal, but is of the opposite polarity. The output voltage signal cancels the power

$$(Y_1 + 1/R_1)U_1 - Y_1U_2 = -I_{eq,1} \quad (3)$$

$$-Y_{n-1}U_{n-1} + (Y_{n-1} + Y_n + 1/R_n)U_n - Y_nU_{n+1} = I_{eq,n-1} - I_{eq,n} \quad (4)$$

$$-Y_{N-1}U_{N-1} + \left(Y_N + \frac{1}{R_{N+1}}\right)U_{N+1} = I_{eq,N} \quad (5)$$

$$[Y][U] = [K][I] \quad (6)$$

$$[Y] = \begin{bmatrix} Y_1 + R_1^{-1} & -Y_1 & & & & \\ -Y_1 & Y_1 + Y_2 + R_2^{-1} & -Y_2 & & & \\ & \dots & \dots & \dots & & \\ & & -Y_{n-1} & Y_{n-1} + Y_n + R_n^{-1} & -Y_n & \\ & & & \dots & \dots & \\ & & & -Y_{N-1} & Y_{N-1} + Y_N + R_N^{-1} & -Y_N \\ & & & & -Y_N & Y_N + R_N + 1 \end{bmatrix} \quad (7)$$

$$[U] = \begin{bmatrix} U_1 \\ U_2 \\ \vdots \\ U_{N+1} \end{bmatrix} \quad (8)$$

$$[K] = \begin{bmatrix} -1 & & & & \\ 1 & -1 & & & \\ & 1 & -1 & & \\ & & \ddots & \ddots & \\ & & & 1 & -1 \\ & & & & 1 \end{bmatrix} \quad (9)$$

$$[I] = \begin{bmatrix} I_{eq,1} \\ I_{eq,2} \\ \vdots \\ I_{eq,N} \\ I_{eq,N+1} \end{bmatrix} \quad (10)$$

$$[U] = [K][I][Y]^{-1} = [K][I][Z] \quad (11)$$

$$I_{g,n} = U_n/R_n \quad (12)$$

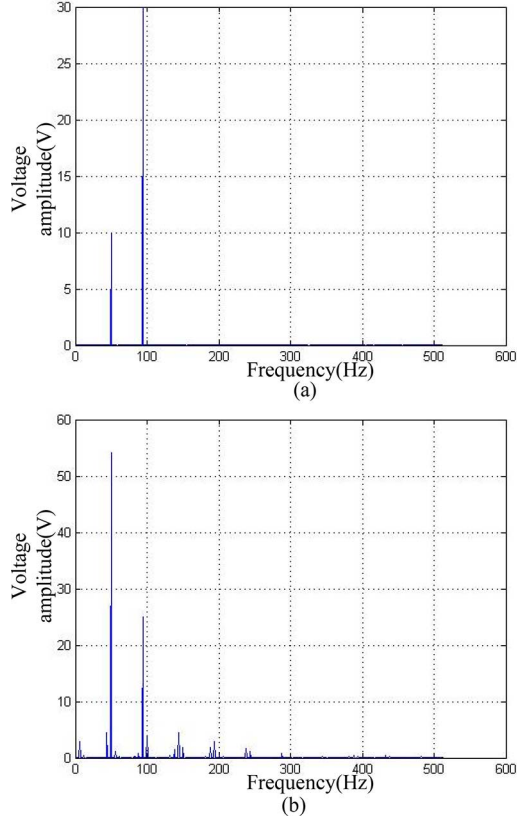


Fig. 5. Frequency spectrum of two output signals (same input) from ADC after being processed by the FFT: (a) when the signal voltage amplitude V_{M1} is smaller than V_R and (b) when the signal voltage amplitude V_{M1} is greater than V_R .

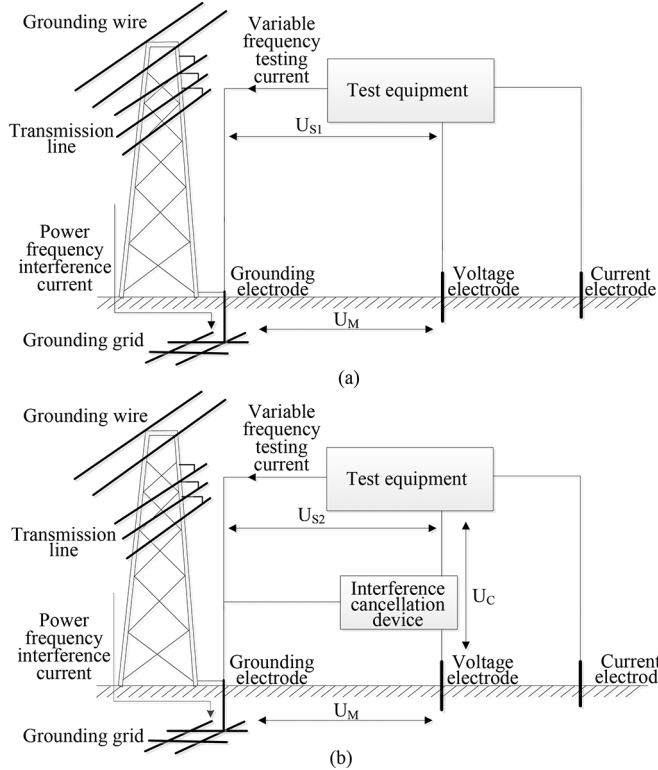


Fig. 6. Principle of RPC method: (a) schematic diagram of the VF method-based measurement and (b) RPC method-based measurement with the addition of the interference cancellation device.

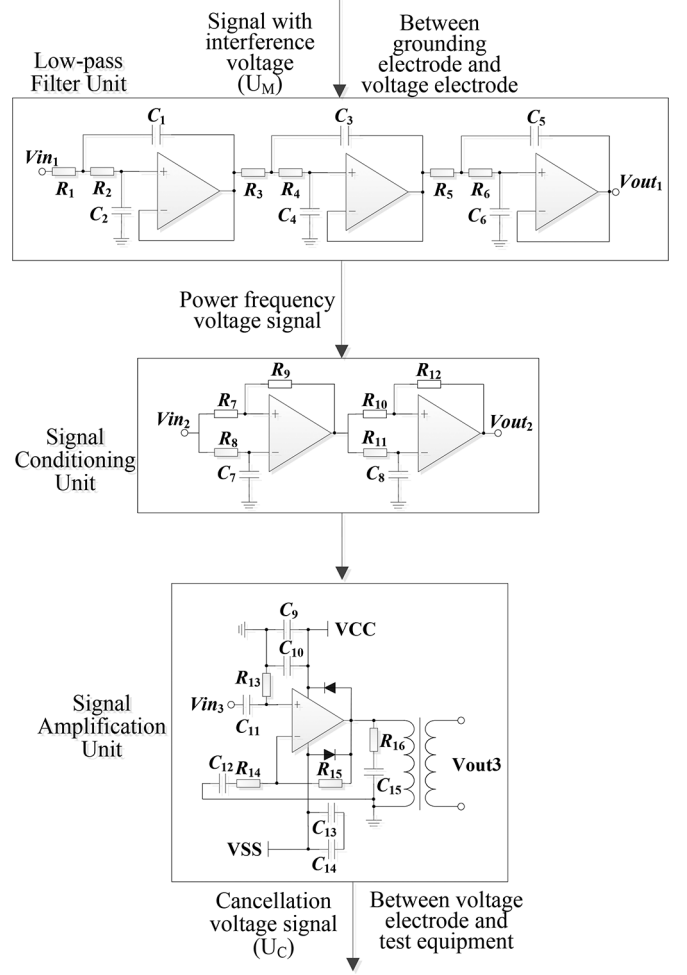


Fig. 7. Operating principle of the interference cancellation device.

frequency interference as (13)–(15) shows. In Fig. 6, U_{S1} and U_{S2} are the voltage signal acquired by the test equipment, U_T is the variable frequency testing voltage, and U_{ST} is the power frequency interference voltage, and U_C is the interference cancellation voltage

$$\dot{U}_{S1} = \dot{U}_M = \dot{U}_{ST} + \dot{U}_T \quad (13)$$

$$\dot{U}_C = -\dot{U}_{ST} \quad (14)$$

$$\dot{U}_{S2} = \dot{U}_M + \dot{U}_C = \dot{U}_T. \quad (15)$$

B. Design Principle of the Interference Cancellation Device

Fig. 7 shows the design principle of the interference cancellation device. The input voltage signal (U_M) is first acquired between the grounding electrode and the voltage electrode before being passed to the low-pass filter unit. The input voltage signal is then analyzed to make sure that the cancellation voltage signal generated in the system always has the same frequency as the interference voltage signal. Tests showed that when the difference between these two signals is greater than 0.04 Hz, the power frequency interference becomes impossible to eliminate. U_M contains a voltage signal at power frequency and the test voltage signal generated by the test equipment. The test voltage frequency is automatically selected from a discrete list of values

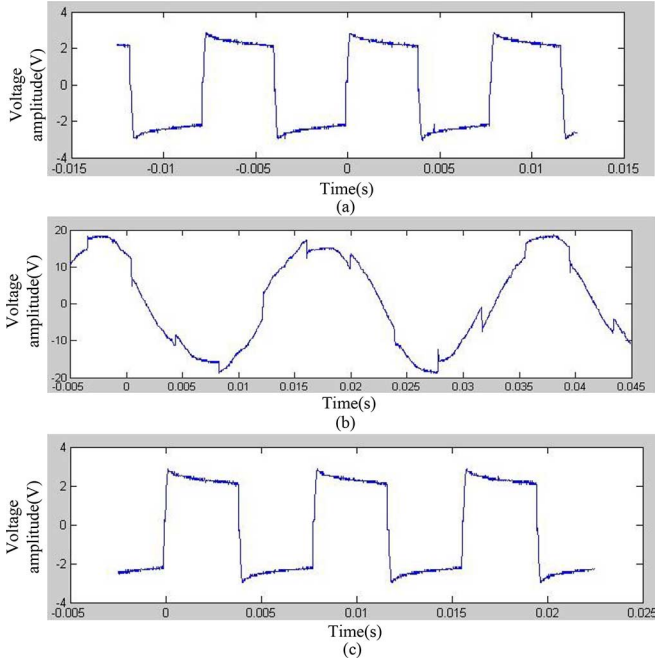


Fig. 8. Voltage signals as measured by the RPC method-based grounding resistance test equipment: (a) the testing voltage signal at the frequency of 128 Hz; (b) the resulting signal containing a signal at power frequency and the testing signal, and (c) the voltage signal measured by grounding resistance test equipment after using the RPC method.

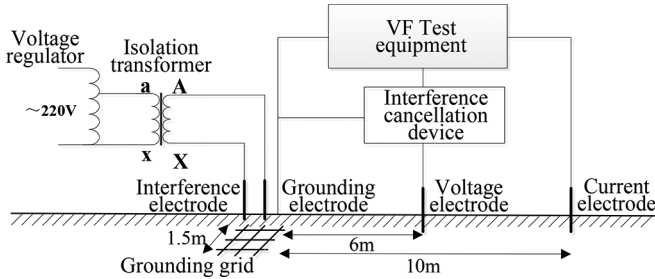


Fig. 9. Schematic diagram of the laboratory test.

(94, 105, 111, and 128 Hz) [25]. A low-pass filter that has a cutoff frequency of 128 Hz is provided to separate the power interference voltage signal and the test voltage signal. An interference cancellation device capable of varying phase and amplitude was designed. The power frequency voltage signal is adjusted by the signal conditioning unit and amplified to generate a cancellation voltage signal (U_C).

Fig. 8 shows that with the application of the RPC method for a test frequency of 128 Hz, the power frequency interference voltage can be removed, while the variable frequency testing signal is left intact.

V. LABORATORY TEST RESULTS

A. Test Setup and Test Method

Tests were carried out at the High Voltage Research Institute at Wuhan University. The schematic diagram of the laboratory test is given in Fig. 9. A voltage regulator and an isolation transformer were used to provide an interference

TABLE I
LABORATORY TEST MEASURING GROUNDING RESISTANCE

External disturbance voltage(V)	Working condition	$U_{ST}(V)$	$R_E(\Omega)$
0	Without RPC method	0	82.4
			82.7
			82.8
			82.5
5	Without RPC method	5	82.7
			83.1
			82.8
			82.7
15.4	With RPC method	0.8	82.7
			82.8
			82.9
			82.7
31	Without RPC method	15.4	83.6
			82.3
			83.4
			83.4
48.7	With RPC method	0.9	83.2
			83.3
			83.3
			83.4
31	Without RPC method	31	Test equipment is deactivated
48.7	With RPC method	0.8	83.5
			83.6
			83.4
			83.2
48.7	Without RPC method	48.7	Test equipment is deactivated
48.7	With RPC method	0.7	83.5
			83.6
			83.4
			83.8

signal. Grounding resistance test equipment adopting the VF method was purchased and employed to measure the grounding resistance. The test voltages were 48 V in all tests. The VF method device used in this paper was always deactivated when external interference voltage exceeded 24 V. By adjusting the output of the interference source, the magnitude of the power frequency interference voltage can be changed. The electrodes were spaced as a straight line. The structure of the grounding grid was square compact grid. The side length of the grounding grid was 1.5 m. The grounding electrode is located above the center of the grounding grid. The spacing between the grounding electrode and voltage electrode was 6 m, and the spacing between the grounding electrode and current electrode was 10 m.

B. Test Results

When the interference cancellation device was connected between the voltage electrode and the test equipment, the interference signal was adjusted to produce the following values: $U_{ST} = 5$ V, 15.4 V, 31 V, and 48.7 V. In order to ensure the accuracy of test results, each value of R_E was measured from separate tests.

Table I gives the results of the laboratory test. The zero interference case is to ensure the consistency of the RPC method. The results show that the RPC method successfully cancels the power frequency interference.

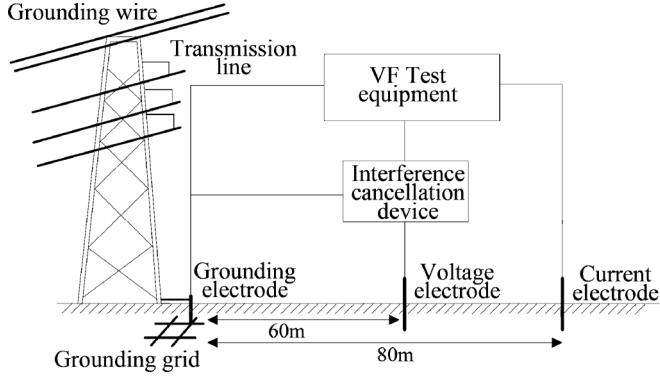


Fig. 10. Schematic diagram of the field test.

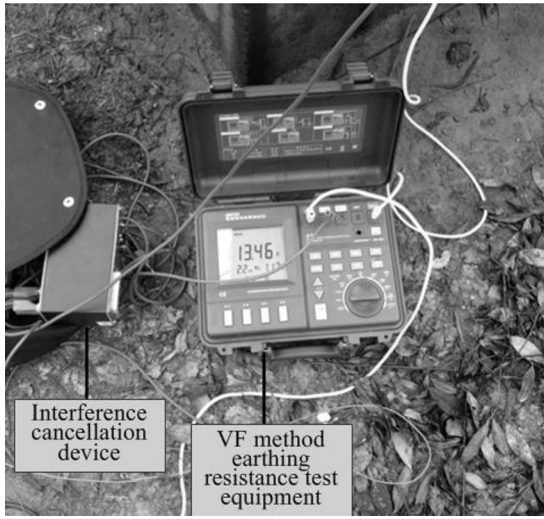


Fig. 11. RPC method in field testing.

VI. FIELD TEST AND TEST RESULTS

A. Test Setup and Test Method

Field tests were performed at four 220-kV transmission towers in a mountainous area in China. Two regular transmission towers and two transposition towers were used, each having two ground wires.

A schematic of the field tests employing the VF method is shown in Fig. 10. The towers were each grounded by means of a radial counterpoise to a different extent, each larger than the compact grounding grid employed in the laboratory tests. The length of all radial counterpoises is about 60 m. The depth of all the radial counterpoises is about 0.8 m. The orientation of the radial counterpoises relative to the test leads and electrodes is parallel. The test electrodes were linearly arranged. The grounding electrode was located in the center of the tower. The spacing between the grounding electrode and the voltage electrode was 60 m, and the distance between the grounding electrode and the current electrode was 80 m in each field test [26]. The orientation of the test leads relative to the transmission line is perpendicular.

B. Field Test Results

Fig. 11 shows the picture of field tests under a transmission tower. Tables II and III summarize the test results for the reg-

TABLE II
GROUNDING RESISTANCE OF THE FIELD TEST AT
REGULAR TRANSMISSION TOWER #1

Working condition	$U_{ST}(V)$	$R_E(\Omega)$
Without RPC method	5.3	13.76
		13.79
		13.74
		13.78
With RPC method	0.3	13.83
		13.85
		13.87
		13.78

TABLE III
GROUNDING RESISTANCE OF THE FIELD TEST AT
REGULAR TRANSMISSION TOWER #2

Working condition	$U_{ST}(V)$	$R_E(\Omega)$
Without RPC method	10.8	18.15
		18.10
		18.13
		18.12
With RPC method	0.4	18.19
		18.21
		18.18
		18.19

TABLE IV
GROUNDING RESISTANCE OF THE FIELD TEST AT
THE PHASE-WIRE TRANSPOSITION TOWER #1

Working condition	$U_{ST}(V)$	$R_E(\Omega)$
Without RPC method	25.3	Test equipment is deactivated
With RPC method	0.8	13.51
		13.48
		13.46
		13.47

TABLE V
GROUNDING RESISTANCE OF THE FIELD TEST AT
PHASE-WIRE TRANSPOSITION TOWER #2

Working condition	$U_{ST}(V)$	$R_E(\Omega)$
Without RPC method	33.6	Test equipment is deactivated
With RPC method	0.8	17.83
		17.88
		17.82
		17.84

ular transmission towers. Results of the phase-wire transposition tower tests are given in Tables IV and V. The results show that the RPC method is capable of cancelling the power frequency interference in the field test, and can enable the VF grounding resistance test equipment to work normally when in conditions of strong interference. The measurement error is less

than 1% with the addition of the RPC device when the interference is below the threshold value. The results also show that the current flowing into the ground from the tower is greater when transmission conductors are transposed.

VII. DISCUSSION

The RPC method has the following improvement by comparing with the interference compensation (IC) method proposed by IEEE standard 81-2 [27].

The signal-to-noise ratio (SNR) of the RPC method is about 36 dB ($\text{SNR}_{\text{RPC}} = 20\lg(V_S/V_N) = 20\lg(50/0.8) \approx 36$ dB). A comparison of field and laboratory test results show that the measurement error introduced by the RPC equipment is in all cases less than 1%. The results also demonstrate that the interference cancellation device operates effectively with different sizes and types of the grounding grid.

The IC method also requires a three-phase ac source, with the compensation voltage signal being manually controlled. The battery-powered RPC equipment has the advantage of portability, and automatic generation of the compensation voltage signal improves ease of use.

VIII. CONCLUSION

When attempting to measure the grounding resistance of an HV transmission tower situated in a mountainous area, the power frequency interference voltage often becomes so excessively high that the VF method would not function. This paper has analyzed the sources and mechanism of power frequency interference during the measurement of transmission tower ground resistance. Theoretical analysis has shown that induced current flowing into the ground is responsible for the interference. The interference voltage may be further increased in the presence of great grounding resistance.

This paper has proposed a reverse-phase cancellation method as a means of mitigating the effect of power frequency interference. The proposed RPC method effectively removes the power frequency interference voltage, and enables measurement of the grounding resistance normally and accurately using VF method grounding resistance test equipment, even in the presence of strong interference.

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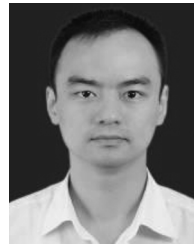
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